

VOLTAGE GENERATOR DEVICE, MOTOR VEHICLE, CONTROL METHOD FOR
THE VOLTAGE GENERATOR DEVICE, CONTROL METHOD FOR THE MOTOR
VEHICLE, AND COMPUTER-READABLE RECORDING MEDIUM STORING
PROGRAM FOR CAUSING COMPUTER TO EXECUTE THE CONTROL METHOD

INCORPORATION BY REFERENCE

[0001] The disclosure of Japanese Patent Application No. 2004-037120 filed on February 13, 2004, including the specification, drawings and abstract is incorporated herein by reference in its entirety.

BACKGROUND OF THE INVENTION

1. Field of the Invention

[0002] The invention relates to a voltage generator device and a motor vehicle that supply an increased drive voltage to an electric load at the time of reduced power supply voltage, a control method for the voltage generator device, a control method for the motor vehicle, and a computer-readable recording medium storing a program for causing a computer to execute such a control method.

2. Description of the Related Art

[0003] Idle-stop vehicles that automatically stop the engine in a temporary fashion during a vehicle stop for a traffic signal or the like have been commercialized in recent years in order to reduce the fuel consumption and the exhaust emissions.

[0004] In such an idle-stop vehicle, the engine is automatically stopped provided that a condition for stopping the engine is met and that the vehicle is considered to be at a stop. Subsequently, when a condition for starting the engine is met, the engine is automatically started by a starter in order to get the idle-stop vehicle under way.

[0005] The starting of the engine by the starter requires a considerable amount of electric power. Therefore, the battery that serves as an electric power supply in the idle-stop vehicle is heavily discharged if the engine is frequently restarted, for example, during city street driving where frequent stops-and-goes are experienced.

[0006] If the engine is started from a stopped state when the battery is significantly discharged, the voltage of the battery temporarily falls to a great extent as the starter consumes electric power. The electric loads connected to the battery include electric loads having a CPU (central processing unit), for example, a car audio device, a car navigation system, etc. If the battery voltage falls below the reset voltage of a CPU (e.g., the power supply voltage of 8V of an electric load), there is possibility that the CPU may be reset and various settings retained during the running of the vehicle may be reset.

[0007] In order to resolve such troubles regarding electric loads, Japanese Patent Application Laid-Open Publication No. 2002-38984 discloses a related-art technology in which if the battery voltage reduces at the time of restart of the engine after an idle stop, the battery voltage is raised from the reduced level before being supplied to an electric load.

[0008] In a conventional arrangement incorporating a DC/DC converter or the like as a voltage generator device, operation of the voltage generator device is stopped for the purpose of circuit protection if an overvoltage or overcurrent exceeding a target value of voltage or current is detected. The stop of operation of this device is immediately followed by a sharp reduction of the output voltage supplied to the electric load, so that the following problem occurs, that is, the voltage compensation cannot be attained and the driving of the electric load cannot be appropriately controlled.

[0009] FIG. 10 is a waveform diagram for describing the output during overcurrent in a related-art technology. With reference to FIG. 10, an example case where a current value of 30 A or greater is set as an overcurrent will be described. Until the output current reaches 30 A, the output voltage remains at a constant voltage (e.g., 12 V) for compensating for the battery voltage fall. As the output current increases, the value of voltage of an overcurrent detection output for detecting an overcurrent state of the output current increases. If this value of voltage exceeds an overcurrent detection threshold value, the operation of the voltage generator device is stopped.

[0010] FIG. 11 is a waveform diagram for describing a control performed during overvoltage in the related-art technology. With reference to FIG. 11, let it assumed that

the output voltage exceeds, for some cause, a target voltage (e.g., 12 V) of a voltage raising operation, and reaches an overvoltage prohibition threshold value (e.g., 16 V) that is close to the withstanding voltage of the electric load. In that case, an overvoltage stop output is output so as to stop the operation of the voltage generator device. Therefore, the input voltage is directly output. With regard to FIG. 11, it is to be noted that the output voltage is slightly lower than the input voltage due to the internal resistance of the voltage generator device.

[0011] Thus, since an ordinary voltage generator device, such as a DC/DC converter or the like, is designed taking a long-time continuous operation into consideration, the operation thereof is generally stopped, for example, for protection of an element or device, if an abnormality is detected. If such a control is applied to a voltage generator device that compensates for a voltage fall at the time of start of the engine after an idle stop, the problem of being unable to appropriately control the driving of a necessary electric load occurs also when the engine of a vehicle having a trouble is started in order to transport the vehicle to a repair shop.

SUMMARY OF THE INVENTION

[0012] The invention has been accomplished in order to solve the aforementioned problem. It is an object of the invention to provide a voltage generator device that prevents occurrence of a problem regarding an electric load by maintaining a voltage raising operation as much as possible and provide a motor vehicle equipped with the voltage generator device.

[0013] It is another object of the invention to provide control methods for the voltage generator device and the motor vehicle, and a computer-readable recording medium storing programs for causing a computer to execute the control methods.

[0014] The voltage generator device of the invention includes a voltage generating portion that receives an input voltage and generates a target voltage, an observing portion that observes an operating condition of the voltage generating portion, and a control portion. The control portion causes the voltage generating portion to maintain the

voltage generating operation even if the operating condition observed by the observing portion is within a first region that is apart from a normal region. If the operating condition observed is within a second region that is further apart from the normal region than the first region is, the control portion causes the voltage generating portion to stop the voltage generating operation.

[0015] In the above-described voltage generator device, it is possible that the operating condition be within the first region if a current through the voltage generating portion is greater than a first value of current and is less than or equal to a second value of current, and that the operating condition be within the second region if the current through the voltage generating portion is greater than the second value of current.

[0016] Furthermore, the control portion may reduce a target value of voltage output by the voltage generating portion if a value of current through the voltage generating portion increases provided that the operating condition is within the first region.

[0017] Still further, it is possible that the operating condition be within the first region if a voltage output by the voltage generating portion is greater than a first value of voltage and is less than or equal to a second value of voltage, and that the operating condition be within the second region if the voltage output by the voltage generating portion is greater than the second value of voltage.

[0018] In this construction, the observing portion may include a voltage detecting circuit that detects the first value of voltage, and an overvoltage detecting circuit that detects the second value of voltage. The control portion may perform on the voltage generating portion a feedback control of setting an output of the voltage generating portion at the target voltage in accordance with an output of the voltage detecting circuit, and may stop the feedback control in accordance with an output of the overvoltage detecting circuit and may prohibit the voltage generating portion from performing the voltage generating operation.

[0019] The motor vehicle of the invention includes an electricity storage means, a voltage generator device that compensates for a fall of an output voltage of the electricity storage means, and an automatic engine stop control means that automatically controls

stopping and starting of an engine. The voltage generator device includes a voltage generating portion that receives an input voltage and generates a target voltage, an observing portion that observes an operating condition of the voltage generating portion, and a control portion which causes the voltage generating portion to maintain a voltage generating operation even if the operating condition observed by the observing portion is within a first region that is apart from a normal region, and which causes the voltage generating portion to stop the voltage generating operation if the operating condition observed is within a second region that is further apart from the normal region than the first region is. The automatic engine stop control means prohibits an automatic stop of the engine if it is detected that the operating condition is within the first region.

[0020] The control method for a voltage generator device of the invention is a control method for a voltage generator device that includes a voltage generating portion that receives an input voltage and generates a target voltage. The control method includes the step of observing an operating condition of the voltage generating portion and causing the voltage generating portion to maintain a voltage generating operation even if the operating condition is within a first region that is apart from a normal region, and the step of causing the voltage generating portion to stop the voltage generating operation if the operating condition observed is within a second region that is further apart from the normal region than the first region is.

[0021] In the method described above, it is possible that the operating condition be within the first region if a current through the voltage generating portion is greater than a first value of current and is less than or equal to a second value of current, and that the operating condition be within the second region if the current through the voltage generating portion is greater than the second value of current.

[0022] In this method, a target value of voltage output by the voltage generating portion may be reduced if a value of current through the voltage generating portion increases provided that the operating condition is within the first region.

[0023] Furthermore, it is possible that the operating condition be within the first region if a voltage output by the voltage generating portion is greater than a first value of

voltage and is less than or equal to a second value of voltage, and that the operating condition be within the second region if the voltage output by the voltage generating portion is greater than the second value of voltage.

[0024] Still further, in the above-described control method, the voltage generator device may include a voltage detecting circuit that detects the first value of voltage, and an overvoltage detecting circuit that detects the second value of voltage. The control method may further include the step of performing on the voltage generating portion a feedback control of setting an output of the voltage generating portion at the target voltage in accordance with an output of the voltage detecting circuit, and the step of stopping the feedback control in accordance with an output of the overvoltage detecting circuit and prohibiting the voltage generating portion from performing an operation.

[0025] The control method for a motor vehicle of the invention is a control method for a motor vehicle that includes a voltage generator device that compensates for a change in an output voltage of an electricity storage means. The control method includes the step of observing an operating condition of the voltage generator device and causing the voltage generator device to maintain a voltage generating operation even if the operating condition is within a first region that is apart from a normal region, the step of controlling stopping and starting of the engine in accordance with a state of the motor vehicle if the operating condition is within the normal region, and the step of prohibiting an automatic stop of the engine if it is detected that the operating condition is within the first region.

[0026] The recording medium of the invention is a computer-readable recording medium in which a program for causing a computer to execute any one of the above-described control methods is recorded.

[0027] The voltage generator device of the invention maintains the voltage raising operation as much as possible even if the operating condition is in a region apart from an ordinary operating condition. Therefore, normal functions of the electric loads connected to the voltage raising device can be maintained as best as possible.

[0028] If the output current exceeds a pre-set maximum rated value of current, the

output voltage is reduced in accordance with increase in the value of current. Therefore, even in the case of overcurrent, voltage compensation can be attained for a short time of starting the engine, and a vehicle failure can be prevented.

[0029] Furthermore, in the case of low voltage, the switching operation is stopped, so that destruction of an element or the like due to heat can be prevented.

[0030] Furthermore, if the output voltage exceeds a pre-set maximum value of overvoltage, the switching operation is intermittently performed. Therefore, even in the case of overvoltage, predetermined output voltage can be maintained, and voltage compensation can be attained for a short time of starting the engine.

[0031] Furthermore, after a control at the time of overcurrent/overvoltage, a warning signal is output and the idle stop is prohibited. Therefore, the voltage generator device can be protected, and the resetting or the like of electric loads of the vehicle can be prevented. Hence, the possibility that the engine can be started at least once without a problem is increased.

BRIEF DESCRIPTION OF THE DRAWINGS

[0032] The above mentioned embodiment and other embodiments, objects, features, advantages, technical and industrial significance of this invention will be better understood by reading the following detailed description of the exemplary embodiments of the invention, when considered in connection with the accompanying drawings, in which:

FIG. 1 is a schematic block diagram of an electric system installed in a motor vehicle according to an embodiment of the invention;

FIG. 2 is a diagram for briefly describing an input-output characteristic of a voltage raising device during a normal operation thereof;

FIG. 3 is a block diagram for describing a construction of the voltage raising device shown in FIG. 1;

FIG. 4 is a diagram for describing a specific example of an operating condition of the voltage raising device;

FIG. 5 is a waveform diagram for describing the operation of the voltage raising device in the case where an overcurrent has occurred;

FIG. 6 is a waveform diagram of the operation of the voltage raising device during overvoltage;

FIG. 7 is a flowchart for describing the operation of a switching control circuit shown in FIG. 3 during overcurrent;

FIG. 8 is a flowchart for describing the operation of the switching control circuit shown in FIG. 3 which is performed in response to overvoltage;

FIG. 9 is a flowchart for describing a control operation of an idle stop ECU shown in FIG. 1;

FIG. 10 is a waveform diagram for describing the output during overcurrent in a related-art technology; and

FIG. 11 is a waveform diagram for describing a control performed during overvoltage in the related-art technology.

DESCRIPTION OF THE EXEMPLARY EMBODIMENTS

[0033] In the following description, the invention will be described in more detail in terms of exemplary embodiments.

[0034] Hereinafter, embodiments of the invention will be described with reference to the accompanying drawings. The same or similar portions are represented by the same reference characters in the drawings, and will not be redundantly described below.

[0035] FIG. 1 is a schematic block diagram of an electric system installed in a motor vehicle according to an embodiment of the invention.

[0036] Referring to FIG. 1, an electric system 100 includes a battery 1, an alternator 2, a starter 3, an EFI (electronic fuel injection) control unit 4, and a voltage raising device 6. The alternator 2, the starter 3, the EFI control unit 4 and the voltage raising device 6 are connected to the positive electrode of the battery 1. The voltage raising device 6 is a voltage generator device that generates and outputs a target voltage that is higher than the input voltage.

[0037] The electric system 100 further includes an idle stop ECU (electrical control unit) 5, an audio/navigation device 7 and an ABS (anti-lock brake system) control unit 9 that are connected to an output of the voltage raising device 6 and that are supplied with a power supply electric current. The electric system 100 also includes an LED (light-emitting diode) 8 for indicating a warning to a driver which is connected to the idle stop ECU 5.

[0038] The alternator 2 receives a power supply voltage VB from the battery 1, and supplies the voltage to a built-in rotor to create a magnetic field. Furthermore, when receiving power from an engine (not shown), the alternator 2 allows the rotor to rotate so as to induce alternating-current electric power in a stator provided around the rotor. Still further, using a built-in diode, the alternator 2 rectifies the induced alternating-current power, and thus converts the alternating-current power into direct-current power. The converted direct-current power is supplied to the battery 1 to charge the battery 1.

[0039] The starter 3 is actuated by the power supply voltage VB from the battery 1 so as to start the engine.

[0040] The EFI control unit 4 controls the supply of fuel to the engine. The voltage raising device 6 receives the power supply voltage of the battery 1 as an input voltage VIN, and raises the voltage, and outputs the raised voltage as an output voltage VOUT. The output voltage VOUT is supplied to electric loads such as the ABS control unit 9, the audio/navigation device 7, the idle stop ECU 5, etc.

[0041] The voltage raising device 6, as described in detail below, is designed so as to be able to supply the output voltage VOUT to the electric loads as much as possible. Therefore, even if the operating region deviates from a normal region to some extent, the electric power supply is maintained so as to allow normal operation of electric loads and, particularly, of electric loads that play important roles in the driving of the vehicle, such as the ABS control unit 9 and the like.

[0042] The idle stop ECU 5 outputs an engine stop control signal STOP to the EFI control unit 4 when an engine stopping condition is fulfilled. Furthermore, if an engine starting condition is fulfilled after the engine stops, the idle stop ECU 5 outputs an engine

start control signal START to the starter 3 so as to start the engine. If a warning signal WARN is transmitted from the voltage raising device 6, the idle stop ECU 5 discontinues the idle stop control, and avoids outputting a control signal STOP. In that case, the idle stop ECU 5 turns on the LED 8 in order to indicate a warning to a driver. The warning indication to a driver may be accomplished by other methods.

[0043] FIG. 2 is a diagram for briefly describing the input-output characteristic of the voltage raising device 6 during a normal operation thereof.

[0044] Referring to FIGS. 1 and 2, the voltage raising device 6 stops its internal switching operation, and outputs a voltage substantially equal to the input voltage via a built-in coil, in the case where the battery voltage is within the range of 0 V to a voltage VIN1 V and the case where the battery voltage is greater than or equal to a voltage VIN2 V.

[0045] If the input voltage is within the voltage range of VIN1 to VIN2, the voltage raising device 6 raises the input voltage to a constant voltage VC, and outputs the constant voltage VC as the output voltage VOUT.

[0046] During an ordinary run of the vehicle, the input voltage VIN is higher than the voltage VIN2 due to the electricity generation performed by the alternator 2. Furthermore, if the battery 1 is sufficiently charged and the electric current consumption of the loads is not great, the input voltage VIN is also greater than the voltage VIN2. Therefore, during an ordinary operation, the voltage raising device 6 does not perform the switching, but is used merely as a passage of electric current.

[0047] In contrast, if the input voltage VIN drops to the range of VIN1 to VIN2 as the power supply voltage of the battery falls due to great power consumption by electric loads as in the case where the engine is started after an idle stop, the voltage raising device 6 raises the input voltage VIN to the output voltage VC, and outputs the output voltage VC.

[0048] For example, it is desirable that the voltage VIN1 be set at, for example, about 6V to 7 V, below the reset voltage of 8V of the CPU contained in an electric load, and that the voltage VIN2 be set at about a normal output voltage of 12 V of the battery 1

which is lower than the generated voltage of the alternator 2. Furthermore, it is preferable that the constant voltage VC be also set at about the normal output voltage of 12 V of the battery 1.

[0049] FIG. 3 is a block diagram for describing a construction of the voltage raising device 6 shown in FIG. 1.

[0050] Referring to FIG. 3, the voltage raising device 6 includes a switching circuit 11 that receives the input voltage VIN, and raises it, and outputs the output voltage VOUT, a shunt resistor 12 for monitoring the current through the switching circuit 11, a current detecting circuit 13 that observes the voltage across the shunt resistor 12 to detect current therethrough, and outputs a current detection signal SI, a voltage detecting circuit 14 that observes the output voltage VOUT and outputs a voltage detection signal SV, and a switching control circuit 16 that performs a switching control in accordance with the current detection signal SI and the voltage detection signal SV.

[0051] The switching circuit 11 includes an electrolytic capacitor 19 connected between a ground node and a node N1 to which the input voltage VIN is given, a coil 20 connected between the node N1 and a node N2, a diode 22 connected between the node N2 and a node N3 from which the output voltage VOUT is output, an electrolytic capacitor 23 connected between the node N3 and a ground node, and a field-effect transistor 21 connected in series with the shunt resistor 12 between the node N2 and the ground node. The gate potential of the field-effect transistor 21 is controlled by the switching control circuit 16.

[0052] The voltage raising device 6 further includes an overvoltage detecting circuit 15 that detects whether or not the output voltage VOUT is greater than a predetermined overvoltage, an overcurrent determining circuit 17 that determines whether or not the current through the switching circuit 11 is greater than a predetermined overcurrent, and a warning output circuit 18 that outputs a warning signal WARN to the idle stop ECU 5 if the overcurrent determining circuit 17 has output an overcurrent detection signal WI or if the overvoltage detecting circuit 15 has detected an overvoltage detection signal WV.

[0053] The operation of the voltage raising device 6 will be briefly described below.

[0054] When the field-effect transistor 21 in the switching circuit 11 becomes conductive, current flows from the node N1 to the ground node via the coil 20 and the transistor 21. During the conductive period of the transistor 21, energy is stored in the coil 20.

[0055] As the transistor 21 changes from the conductive state to a non-conductive state, the energy stored in the coil 20 up to the moment is released. If this results in the electric potential of the node N2 being higher than that of the node N3, forward current flows through the diode 22. If the forward current exceeds the amount of current consumed by the node N3, the electric potential of the node N3 rises.

[0056] The switching control circuit 16 has, for example, a transmitter circuit therein, and therefore makes the field-effect transistor 21 conductive in a predetermined cycle. The period of conduction of the transistor 21 is determined by monitoring the current detection signal SI output by the current detecting circuit 13 and the voltage detection signal SV output by the voltage detecting circuit 14.

[0057] Hence, during an ordinary operation, the switching control circuit 16 detects the present value of the output voltage VOUT, and detects how much energy presently needs to be stored in the coil 20 or how much energy is presently stored therein, and therefore determines how long the conduction through the transistor 21 needs to be continued, and accordingly controls the on/off duty ratio of the field-effect transistor 21.

[0058] Thus, the switching control circuit 16 performs a first feedback control by using the current detecting circuit 13 and the voltage detecting circuit 14.

[0059] The switching control circuit 16 also performs a second feedback control in case of occurrence of an abnormality in the first feedback control. That is, if the overvoltage detecting circuit 15 detects overvoltage and outputs a switching prohibition signal ENV, the switching control circuit 16 stops the switching operation of the transistor 21. As a result, the voltage raising operation performed by the switching circuit 11 stops.

[0060] If the overcurrent determining circuit 17 receives from the switching control circuit 16 a signal DI that indicates the on/off duty ratio of the transistor 21, and

determines that the conductive period of the transistor 21 is longer than a predetermined value and therefore that the voltage raising capability of the switching circuit 11 is approaching its limit, then the overcurrent determining circuit 17 outputs an overcurrent determination signal OC. Upon detection of overcurrent, the switching control circuit 16 makes the target voltage of the voltage raising operation lower than a predetermined value, and accordingly causes execution of the voltage raising operation so that the value of current reduces.

[0061] Although in FIG. 3, the overcurrent determining circuit 17 detects the overcurrent by receiving from the switching control circuit 16 a signal DI that indicates the on/off duty ratio of the transistor 21, the overcurrent may instead be detected through the provision of a shunt resistor or a Hall element between the node N3 and an electric load connected thereto.

[0062] FIG. 4 is a diagram for describing a specific example of the operating condition of the voltage raising device.

[0063] Referring to FIG. 4, the range of output current of 0 A to 30 A is defined as an ordinary operating condition, and the range of output current of 30 A to 45 A is defined as an cautious operating condition, and the range of output current above 45 A is defined as an operation stopping condition. With regard to voltage, the range of output voltage of 0 V to 12 V is defined as an ordinary operating condition, and the range of output voltage of 12 V to 16 V is defined as a cautious operating condition, and the range of output voltage above 16 V is defined as an operation stopping condition.

[0064] If there is no abnormality in the voltage raising device 6 or any one of the electric loads connected thereto, the operating point is any point on a line segment CD.

[0065] The case where an overcurrent has occurred refers to a case where the current consumed by a load connected to the output of the voltage raising device has increased due to a quasi-contact of a harness or the like. In that case, if the output current increases, a reduction of the target value of the voltage raise will maintain a certain voltage raising operation. For example, if the maximum value of the voltage raising capability of the switching circuit 11 is at point D, the voltage raising operation is

maintained by reducing the output voltage within such an extent that the present electric power is not exceeded. That is, during the overcurrent state of the output current, the operating point exists on a line segment DE.

[0066] If the voltage is reduced below point E, the generated voltage of the voltage raising device becomes lower than the reset voltage of 8 V of the CPU of the electric load. Therefore, the advantage of the voltage raising operation disappears, and the operation is stopped. In FIG. 4, 45 A by 8 V is equivalent to the electric power of 30 A by 12 V, and 45 A is indicated as an example of I_2 .

[0067] Next, conditions in which the output voltage becomes overvoltage will be described. During an ordinary operation, the target value of the raised voltage of the voltage raising device is 12 V, and therefore the output voltage does not exceed 12 V. However, if the current detecting circuit 13 or the voltage detecting circuit 14 in FIG. 3 has a great detection error or an offset in the detected value, the output voltage may exceed 12 V.

[0068] In such a case, the voltage raising operation is maintained despite deviation of the output voltage from a normal range, substantially as long as the output voltage does not exceed 16 V. In this case, the operating point is within the region between the line segment AB and the line segment CD. However, if the output voltage exceeds 16 V, there arises a possibility that the withstanding voltage of an electric load connected to the output of the voltage raising device may be exceeded, and therefore the operation is stopped.

[0069] FIG. 5 is a waveform diagram for describing the operation of the voltage raising device in the case where an overcurrent has occurred.

[0070] Referring to FIG. 5, as long as the output current does not exceed, for example, 30 A, the output voltage V_{OUT} remains at a predetermined target value of, for example, 12 V. During that state, as the output current increases, the value of the overcurrent detection output signal DI output by the switching control circuit 16 shown in FIG. 3 also increases.

[0071] When the value of the overcurrent detection output signal DI reaches a value

corresponding to a current of 30 A, the overcurrent determining circuit 17 outputs a signal OC to the switching control circuit 16, thereby commanding the circuit 16 to reduce the target voltage of the voltage raising operation in accordance with the increase in the value of current.

[0072] Hence, if the output current exceeds 30 A, the switching control circuit 16 reduces the voltage by reducing the duty ratio of the switching. If the output voltage reduces to or below 8 V, the operation of the voltage raising device 6 is stopped because, under this condition of output voltage, the reset voltage of the CPU of an electric load connected to the output of the voltage raising device 6 is fell short of and the continued operation of the voltage raising device 6 will cause a failure of the load.

[0073] FIG. 6 is a waveform diagram of the operation of the voltage raising device during overvoltage.

[0074] As indicated in FIG. 6, after the raising of the input voltage V_{IN} is started for some reason, the output voltage V_{OUT} may sometimes exceed the target value, for example, 12 V, and may reach the overvoltage prohibition threshold value of 16 V. If, due to an abnormality, the output voltage V_{OUT} exceeds the target voltage and reaches the overvoltage prohibition threshold value, the overvoltage detecting circuit 15 shown in FIG. 3 activates the switching prohibition signal ENV. As a result, the switching control circuit 16 turns off the transistor 21, so that the output voltage V_{OUT} decreases as current is consumed by a load provided on the output side.

[0075] Then, the overvoltage detecting circuit 15 deactivates the prohibition signal ENV, so that the switching control circuit 16 starts the switching of the transistor 21 again. As a result, the output voltage V_{OUT} rises. As this operation is repeatedly performed, the switching circuit 11 intermittently performs the voltage raising operation.

[0076] FIG. 7 is a flowchart for describing the operation of the switching control circuit 16 shown in FIG. 3 during overcurrent.

[0077] Referring to FIG. 7, after the operation starts, it is determined in step S1 whether the value of current I through the switching circuit 11 is less than or equal to a current I_1 . If the value of current I is less than or equal to I_1 , the process proceeds to

step S2, in which the target value of the output voltage is set at an ordinary target value (e.g., 12 V) and the switching control is performed.

[0078] Conversely, if it is determined in step S1 that the value of current I is greater than the value of current I_1 , the process proceeds to step S3. In step S3, it is determined whether the value of current I , which is now greater than I_1 , is less than or equal to a value of current I_2 .

[0079] If it is determined in step S3 that $I_1 < I \leq I_2$, the process proceeds to step S4, in which the switching control circuit 16 performs the switching control so as to decrease the output voltage in accordance with the increase in the value of current.

[0080] Conversely, if it is determined in step S3 that $I_1 < I \leq I_2$ is not the case, that is, if it is determined that $I > I_2$, the process proceeds to step S5, in which the switching operation is stopped. After step S2, S4 or S5 is completed, the process returns to the observation of the value of current in step S1.

[0081] Through this control, the voltage raising operation based on the operation waveform shown in FIG. 5 is performed.

[0082] FIG. 8 is a flowchart for describing the operation of the switching control circuit 16 shown in FIG. 3 which is performed in response to overvoltage.

[0083] Referring to FIG. 8, after the control starts, it is determined in step S11 whether the output voltage $V_{OUT} \leq V_1$. If $V_{OUT} \leq V_1$, the process proceeds to step S12, in which the switching control circuit 16 sets the target value of output voltage at an ordinary target value, for example, 12 V, and performs the switching control.

[0084] Conversely, if it is determined in step S11 that $V_{OUT} \leq V_1$ is not the case, the process proceeds to step S13, in which it is determined whether $V_1 < V_{OUT} \leq V_2$. If it is determined in step S13 that $V_1 < V_{OUT} \leq V_2$ is fulfilled, the process proceeds to step S14, in which the present voltage raising operation is maintained until the output voltage V_{OUT} exceeds the voltage V_2 .

[0085] Conversely, if it is determined in step S13 that $V_1 < V_{OUT} \leq V_2$ is not fulfilled, which means that the output voltage V_{OUT} is greater than V_2 , the process proceeds to step S15, in which the switching is stopped. After step S12, S14 or S15 is completed,

the process returns to step S11, in which determination regarding voltage is performed. Through the execution of the foregoing operation, the operation waveform shown in FIG. 6 is observed.

[0086] If the switching control circuit 16 shown in FIG. 3 is realized by a computer, the switching control circuit 16 is equipped with a built-in ROM (read-only memory) in which programs for causing operations illustrated by the flowcharts of FIGS. 7 and 8 have been written.

[0087] FIG. 9 is a flowchart for describing a control operation of the idle stop ECU 5 shown in FIG. 1.

[0088] Referring to FIG. 9, after the control starts, it is determined in step S21 whether an engine stopping condition is fulfilled. The fulfillment of the engine stopping condition is determined, for example, if the remaining charge of the battery is greater than or equal to a predetermined value, or if the motor vehicle has a vehicle speed history that indicates the running of at least a predetermined distance, or if the upslope angle is within a predetermined angle in the case where the motor vehicle is running uphill, or the like.

[0089] If it is determined in step S21 that the engine stopping condition is not fulfilled, the fulfillment of the engine stopping condition is awaited. Conversely, if it is determined in step S21 that the engine stopping condition is fulfilled, the idle stop control is performed in step S22. Specifically, the idle stop ECU 5 shown in FIG. 1 outputs a signal STOP commanding the EFI control unit 4 to stop the supply of fuel.

[0090] After the end of step S22, the process proceeds to step S23, in which it is observed whether an engine starting condition is fulfilled. The fulfillment of the condition for starting the engine after an idle stop is determined on the basis of the off-state of the brake pedal and the on-state of the accelerator.

[0091] If it is determined in step S23 that the engine starting condition is not fulfilled, the fulfillment of the engine starting condition is awaited. Conversely, if it is determined in step S23 that the engine starting condition is fulfilled, the process proceeds to step S24, in which an engine starting control is performed.

[0092] Specifically, the idle stop ECU 5 outputs to the starter 3 a signal START that commands the starter 3 to start the engine. After step S24 is completed, that is, after the engine is started, the process proceeds to step S25. In step S25, it is determined whether the idle stop ECU 5 has received a warning signal WARN from the voltage raising device 6 up to the present time.

[0093] If it is determined in step S25 that the idle stop ECU 5 has not received the warning up to the present, the process returns to step S21, in which the idle stop ECU 5 waits for the fulfillment of the engine stopping condition. Conversely, if it is determined in step S25 that the idle stop ECU 5 has received the warning signal WARN from the voltage raising device 6, the idle stop ECU 5 proceeds to step S26. In step S26, the idle stop ECU 5 turns on the warning LED 8 in order to indicate an abnormality to the driver. The lighting of the LED in step S26 is not essential, but other measures may be taken to indicate an abnormality to the driver.

[0094] Subsequently in step S27, the idle stop ECU 5 prohibits the idle stop control from then on. Therefore, if the engine stopping condition is fulfilled during a wait for a traffic signal or the like, the engine will not be stopped, so that the reduction in the battery voltage due to the power consumed by the starter 3 will be avoided and the operation of the voltage raising device 6 will also be avoided. Hence, the driver is allowed to drive the vehicle having an abnormality to a repair shop or the like.

[0095] If the idle stop ECU 5 shown in FIG. 1 is realized by a computer, the idle stop ECU 5 is equipped with a built-in ROM (read-only memory) in which a program for causing an operation as illustrated by the flowchart of FIG. 9 has been written.

[0096] The embodiments disclosed above should be considered to be merely illustrative in all respects and not restrictive at all. The scope of the invention is not restricted by the foregoing description of embodiments, but is defined only by the appended claims, and is intended to cover all modifications within the scope of the appended claims and equivalents thereto.